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### **68000**

#### **Microprocessor**

The Motorola 68000 ("sixty-eight-thousand"; also called the "Texas Cockroach", the m68k or Motorola 68k, "sixty-eight-kay") is a 16/32-bit CISC microprocessor, introduced in 1979 by

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## Microprocessor

Motorola

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Products Sector. The

design implements a

32-bit instruction set,

with 32-bit registers

and a 32-bit internal

data bus.

### **Motorola 68000 -**

### **Wikipedia**

The Motorola 68000

series (also known as

680x0, m68000, m68k,

or 68k) is a family of

32-bit CISC

microprocessors.

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During the 1980s and early 1990s, they were popular in personal computers and workstations and were the primary competitors of Intel 's x86 microprocessors.

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Designed to demystify the Motorola 68000 microprocessor—its hardware and software—this detailed reference leads users

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on an in-depth, hands-on exploration of more than 75 different applications and then guides them through the construction and programming of their own working single-board 68000 system.

### **The 68000 Microprocessor (5th Edition): Antonakos, James L ...**

Motorola 68000  
(MC68000) is the first  
member of 680x0 line

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of microprocessors.

Internally the 68000 is a 32-bit microprocessor - it has 32-bit data and address registers.

Externally the processor has 16-bit data bus and 24-bit address bus, which limits the size of addressable memory to 16 MB.

### **Motorola 68000 microprocessor family - CPU World**

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## **MOTOROLA 68000 INSTRUCTION SET Pdf Download | ManualsLib**

The Motorola 68000  
Instruction Set can  
directly access up  
to.16 Mbytes of  
memory with its 24-bit  
Address Bus. The  
memory space may be

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expanded to 64 Mbytes by using the function code lines (FC0-FC3). To interface 16 Mbytes of memory 68000 provides A1to A23(23) address lines along with UDS and LDS signals. The interface is shown in the Fig. 11.7.

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RAM and 128kB ROM.  
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4 The 68000's  
Instruction Set  
Application: To add to the contents of an address register and not update the CCR.

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Note that ADDA.W  
D0,A0 is the same as  
LEA (A0,D0.W),A0.  
Condition codes: X N Z  
V C An ADDA operation  
does not affect the  
state of the CCR.  
Source operand  
addressing modes

### **The 68000's Instruction Set**

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1993 M68000  
8-/16-/32-Bit  
Microprocessors User's  
Manual  $\mu$  Motorola

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microprocessor  
interfacing techniques  
used in industry today.  
From the Back Cover

**68000**

**Microprocessor:**

**MacKenzie, I. Scott:**

**9780023736544 ...**

MOTOROLA INC., 1992

MOTOROLA M68000

FAMILY Programmer's

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(Includes CPU32

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## Microprocessor **Semiconductors**

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programming of their own working single-board 68000 system.

## **68000 Microprocessor / Edition 5 by James L. Antonakos ...**

The Sega Mega Drive (Genesis in the U.S.) used a 68000 as its main processor. Its successor, the Saturn, used one as its sound processor. The Sequential Circuits Prophet VS, Ensoniq

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EPS, and several other synthesizers used the 68000 in the late 80's. The Atari Jaguar had a 68000 as the central CPU among many dedicated processors.

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